Software Reliability

Lecture 3

Static Program Verification (ctd.)

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Static program verification so far

Recap:

Static verification of call- and loop-free programs: done

- SSA conversion
- Predication to handle conditionals
- SMT-LIB formula generation
- Invoke SMT solver

Now let us see how to handle procedure calls and loops

We start by introducing two new statements: **havoc** and **assume**



havoc v; Sets **v** to an **arbitrary** value

Sometimes written as: v = *;

int x = 5; havoc x; assert(x == 5); // May fail int x = 5; havoc x; assert(x != 5); // May also fail

What's the point of havoc?

Allows us to **over-approximate** parts of programs



foo_abstract captures all the behaviours of foo, and more
foo_abstract correct => foo correct

Over-approximation: if, not iff



foo_abstract captures all the behaviours of foo, and more
We may have foo_abstract incorrect but foo correct

Modifies sets again

Recall:

modset(*S*) returns variables that are possibly modified by statement *S*:

- $modset(v = E) = \{v\}$
- modset(assert E) = { }
- $modset(S; T) = modset(S) \cup modset(T)$
- $modset(if(E) \{S\} else \{T\}) = modset(S) \cup modset(T)$

Let us add:

• modset(havoc V) = { V }

And while we are at it:

modset(while (E) { S}) = modset(S)

Put simply: statement S possibly modifies v if v = e or havoc v occurs in S if and while are

if(e) x = 4; else y = 5;

possibly modifies { x, y }

```
while(x < 100) {
    if((x % y) == 0) {
        havoc z;
        y = y - 1;
    }
    x = x + 1;
}</pre>
```

possibly modifies { x, y, z }

compound

statements

modset(*S*): variables **possibly modified** by *S*

Suppose:

- Statement S appears in program P
- S contains no assertions
- **modset**(*S*) = { $V_1, V_2, ..., V_n$ }

Program P' – identical to P, but S replaced with:

```
havoc V_1; havoc V_2; ...; havoc V_n; havoc: an extreme
```

P' over-approximates P

If **P**' is **correct** then **P** is **correct**

havoc: an extreme form of overapproximation – we will see less extreme forms

If P' is incorrect then P may or may not be incorrect





No-op if e is true Blocks execution if e is false

Can be thought of as equivalent to:

Like **havoc**, only really makes sense in the context of program analysis

int x = 5; assume(x == 4); assert(x == 0); // CORRECT! int x = 5; assume(x > 0); assert(x == 0); // INCORRECT!

Which assume statement unconditionally blocks execution?

What's the point of assume?

Allows us to under-approximate parts of programs



foo_constrained captures a subset of foo's behaviours
foo_constrained incorrect => foo incorrect

Under-approximation: if, not iff



foo_constrained captures a subset of foo's behaviours
We may have foo_constrained correct but foo incorrect

Under-approximation using assume

Suppose program **P** contains statement S

Program **P'** – identical to **P**, except S replaced with:

assume e; S

P' under-approximates P

- If P' is incorrect then P is incorrect
- If P' is correct, P may or may not be correct

P' over-approximates P



P' under-approximates P



Both can be used to simplify analysis

Over-approximation:

- may enable a proof of correctness
- adds program behaviours may add bugs, leading to false positives

Under-approximation:

- may enable detection of bugs
- removes program behaviours may remove genuine bugs, leading to false negatives

Over-approximation using havoc and assume

Havoc is extreme – we can use **assume** to make it less so



Captures the **possible** effects of the conditional Loses relationship between value of **t** and condition **y > z**

Handling calls: inlining

//@ requires/ensures/modifies: JML notation



Handling calls: summarisation



...but the over-approximation is too coarse – false positive

Improving summary using bar's post-condition



Exploiting bar's post-condition eliminates the false positive here

Ally's Summarisation in general: first attempt

Call statement:

 $v = bar(e_1, ..., e_n);$

Specification (a.k.a. contract) for bar:

//@ requires P; //@ modifies $g_1, \ldots, g_m;$ //@ ensures Q; int bar(int p_1, \ldots , int p_n);

Replace call with pre-condition assertion and summary:

 $\begin{array}{l} \textbf{assert}(P[e_1/p_1, \ \dots, \ e_n/p_n]);\\ \textbf{havoc} \ g_1; \ \dots; \ \textbf{havoc} \ g_m;\\ \textbf{havoc} \ v;\\ \hline \textbf{assume}(Q[v \ / \ result]); \end{array}$

Assert pre-condition Havoc **modset** Havoc receiving variable Assume post-condition This is not quite right

Problem with first attempt



Replacing bar with summary (according to previous slide) gives:

```
void foo(int x) {
   havoc c; // havoc receiving variable
   assume((\result != c)[c / \result]); // assume post-condition
   assert(false);
}
```

Simplifies to: void foo(int x) { havoc c; // havoc receiving variable assume(c != c); // !!! assert(false); } Correct So this is **not** a sound overapproximation

Correct summarisation

Fix: Introduce fresh temporary variable, **bar_ret**, to capture post-condition of **bar**

Call statement:

 $v = bar(e_1, ..., e_n);$

Specification for bar:

//@ requires P; //@ modifies $g_1, \ldots, g_m;$ //@ ensures Q; int bar(int p_1, \ldots , int p_n);

Replace call with **pre-condition assertion** and **summary**:

```
\begin{array}{l} assert(P[e_1/p_1, \ \dots, \ e_n/p_n]);\\ havoc \ g_1; \ \dots; \ havoc \ g_m;\\ havoc \ bar\_ret;\\ assume(Q[bar\_ret \ / \ result]); \end{array}
```

v = bar_ret;

Assert pre-condition Havoc **modset** Havoc return temp Assume post-condition **for return temp** Copy into receiving variable

Soundness of summarisation

foo calls bar

When verifying foo we replace bar by its summary

Is this sound?

Yes, as long as we also verify bar

bar's summary over-approximates bar if bar is correct

If **bar** is **not** correct then it may be **unsound** to assume **bar**'s post-condition

Result: modular verification only succeeds if we manage to verify **all** procedures

Note: coarse summarisation without assuming postcondition is always OK (though not often useful)



Can you work out how to adapt summarisation to support post-conditions that use **\old** to refer to the values held by global variables on procedure entry?

Sound analysis depends on being able to compute **modifies** sets for procedures

Why is this **easy** in Simple C?

Why is it **not at all easy** in Java or C?

Given program with procedures and procedure calls, but no loops:

Use **summarisation** to over-approximate calls

Every procedure is now loop- and call-free

Apply techniques of last lecture for verification

Problem: how do we treat our new friends, **havoc** and **assume**, during **SSA conversion**?

SSA conversion for havoc

This is trivial

- "havoc v" means: "forget everything about v"
- How do we do that during SSA conversion?
- Just give v a fresh SSA id

```
y = x + 1;
havoc x;
assert y == x + 1;
```

 $y_1 = x_0 + 1;$ // increment x's id assert $y_1 == x_1 + 1;$

SSA conversion for assume

Recall: "assume e" blocks execution unless e holds

Our goal in verification is to check assertions so:

- If an **assume** fails...
- ... no **assertion** should be checked afterwards

We can account for this by tracking a set of **global assumptions**, and guarding assertions by these assumptions

SSA conversion for assume: example



guarded by the assumptions

Updated SSA conversion algorithm

We had:

```
toSSA(S, Pred, M)
```

We now have:

toSSA(*S*, *Pred*, *Assumptions*, *M*)

Assumptions is **passed by reference**: assumptions grow as we translate **assume** statements

Top-level statement *S* is converted by executing: **toSSA**(*S*, true, true, *init*) where *init* maps each variable to SSA id 0.

Updated SSA conversion algorithm

```
toSSA(v = E, Pred, Assumptions, M) {
 newld := fresh(v);
                                    Only check the
 emit("v_{newld} = apply(E, M); ");
                                    assertion if the current
 M(v) := newld;
                                    assumptions hold
toSSA(assert E, Pred, Assumptions, M) {
 emit("assert (Assumptions & Pred) ==> apply(E, M); ");
toSSA(S; T, Pred, Assumptions, M) {
 toSSA(S, Pred, Assumptions, M);
 toSSA(T, Pred, Assumptions, M);
```

```
toSSA(if(E) \{ S \} else \{ T \}, Pred, Assumptions, M) \}
       NewPred := apply(E, M);
       M' := M.clone();
       M'' := M.clone();
       toSSA(S, Pred & & NewPred, Assumptions, M');
       // omit if else branch is empty
       toSSA(T, Pred & & ! (NewPred), Assumptions, M'');
       for(v: modset(S) \cup modset(T)) 
              M(v) := \mathbf{fresh}(v);
              emit("v_{M(v)} = NewPred ? v_{M'(v)} : v_{M''(v)}");
       }
```

New cases for **havoc** and **assume**:

toSSA(havoc v, Pred, Assumptions, M) {
 M(v) := fresh(v);
}

toSSA(assume E, Pred, Assumptions, M) {
 Assumptions := Assumptions && (Pred ==> apply(E, M));

Recall that Assumptions is passed **by reference** Add the predicated assume condition as a new assumption

```
int foo(...)
  requires R_1, ..., requires R_m,
  ensures E_1, ..., ensures E_n {
      S;
      return e;
                               Suppose that
}
                               specifications for all
                               procedures called
                               inside S are available
```

First, rewrite:

- preconditions as assumes
- postconditions as asserts

```
int foo(...)
  requires R_1, ..., requires R_m,
  ensures E_1, ..., ensures E_n {
        S;
       return e;
}
assume R_1; ...
assume R_m,
S;
assert E_1[e/|result]; ...
assert E_n[e/ \text{result}];
```

Next, replace S with summarise(S) – apply summarisation to each call in S

assert $E_n[e/ \text{result}];$

assume R_1 ; ...

summarise(S);

assume R_m ,

```
int foo(...)
                              requires R_1, ..., requires R_m,
                              ensures E_1, ..., ensures E_n {
                                   S:
                                  return e;
                            }
                                 assume R_1; ...
                                 assume R_m,
                                 S;
assert E_1[e/|result]; ...
                                 assert E_1[e/|result]; ...
                                 assert E_n[e/|result];
```

We now have a loopfree, call-free program:

```
assume R_1; ...

assume R_m,

summarise(S);

assert E_1[e/\result]; ...

assert E_n[e/\result];
```

Apply **SSA conversion**, turn the program into a formula, and ask an SMT solver whether the formula is **satisfiable**

Formula **unsat** => original program is **correct**

Why => and not <=>?



Simple procedure with loop

```
void foo(int x) {
    int i;
    i = 0;
    while(i < x) {
        i = i + 1;
     }
     assert(i == x);
}</pre>
```

Obviously (to a human) correct

?

Not for all values of **x**

Simple procedure with loop

```
void foo(int x)
  requires x > 0 {
    int i;
    i = 0;
    while(i < x) {
        i = i + 1;
     }
    assert(i == x);
}</pre>
```

Now it is correct.

How do we verify it?

Over-approximating loops: first attempt

First idea: replace loop with statements that havoc the loop body's **modset**



Problems:

- 1. only sound if loop body does not contain assertions
- 2. leads to a very coarse over-approximation (too coarse here)

Over-approximating loops: first attempt

Illustration of problem (1)



Havocking does not yield an over-approximation – we miss bugs that occur in the loop body

Over-approximating loops: second attempt

Check an **arbitrary** loop iteration



Over-approximating loops: third attempt



A stronger invariant allows verification to succeed



When checking code immediately after a loop we can assume that the loop invariant holds

Can provide a much better summary of the loop than obtained by only havocking the modset

We make this sound using **induction**:

Prove invariant holds on **loop entry** (**base case**)

Assume invariant holds for **arbitrary iteration** (**induction hypothesis**)

Prove invariant holds at end of iteration (step case)

Over-approximating loops: general case



Given set of procedures, specification for each procedure and invariant for every loop:

Replace each procedure call with summary according to callee's specification

Replace each loop with summary according to loop's invariant

Every procedure is now loop- and call-free

Apply assume- and havoc-aware SSA conversion

Build verification condition formula

Give it to SMT solver